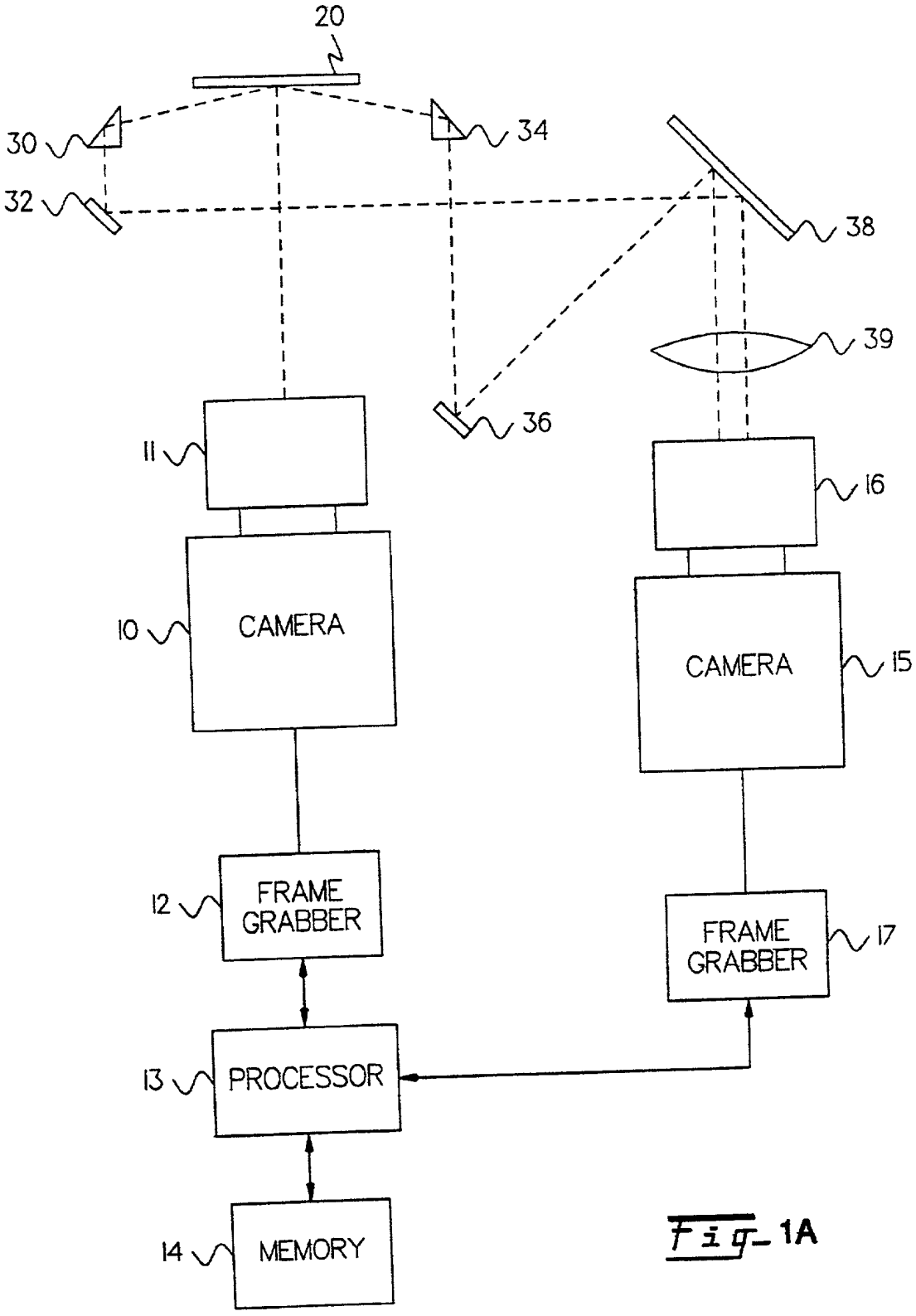

Fig. 1A

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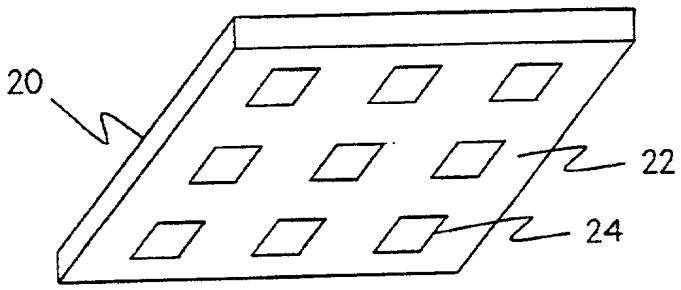
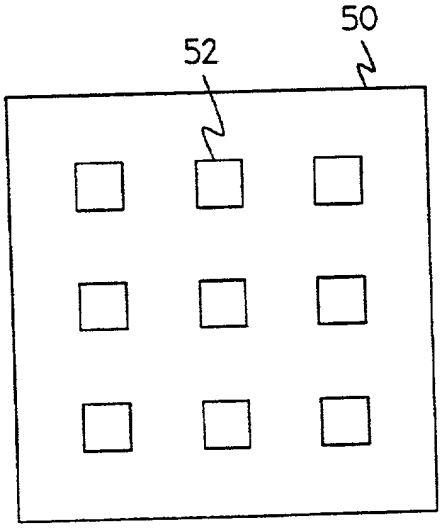


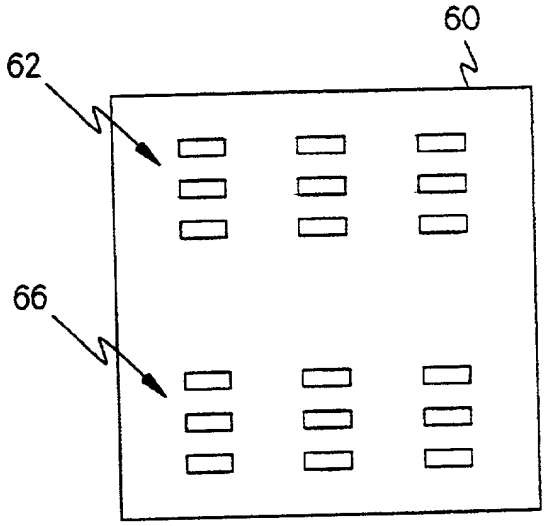
CALIBRATION
RETICLE

Fig. 1B1



BOTTOM VIEW
IMAGE

Fig. 1B2



2 SIDE VIEWS
IMAGE

Fig. 1B3

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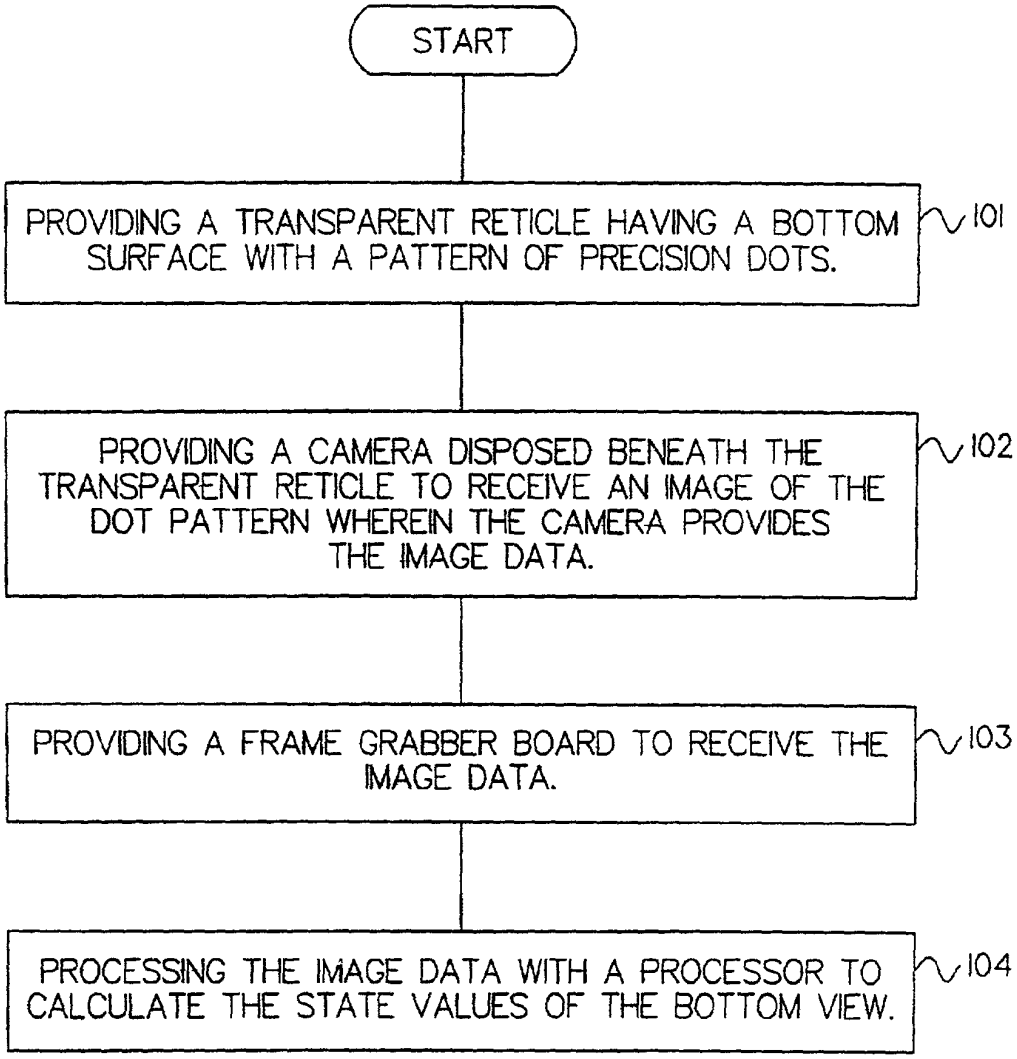

Fig. 2A

09815355 - 052702

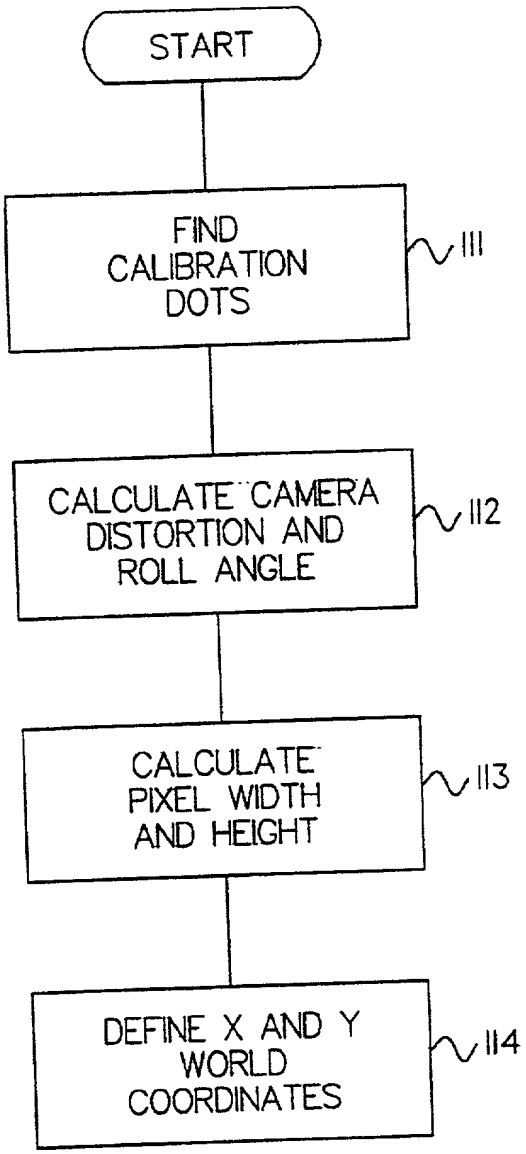

Fig. 2B

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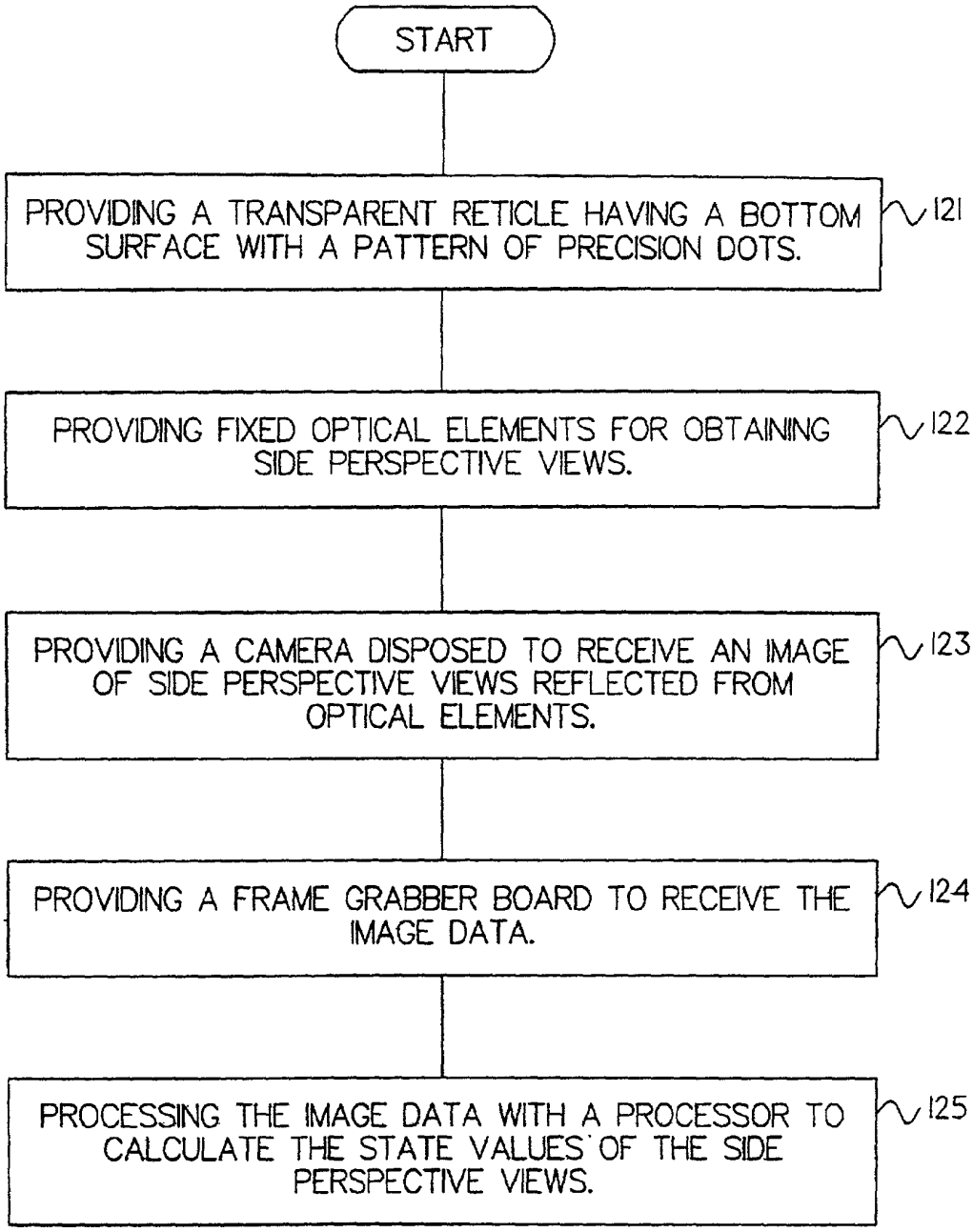

Fig. 2C

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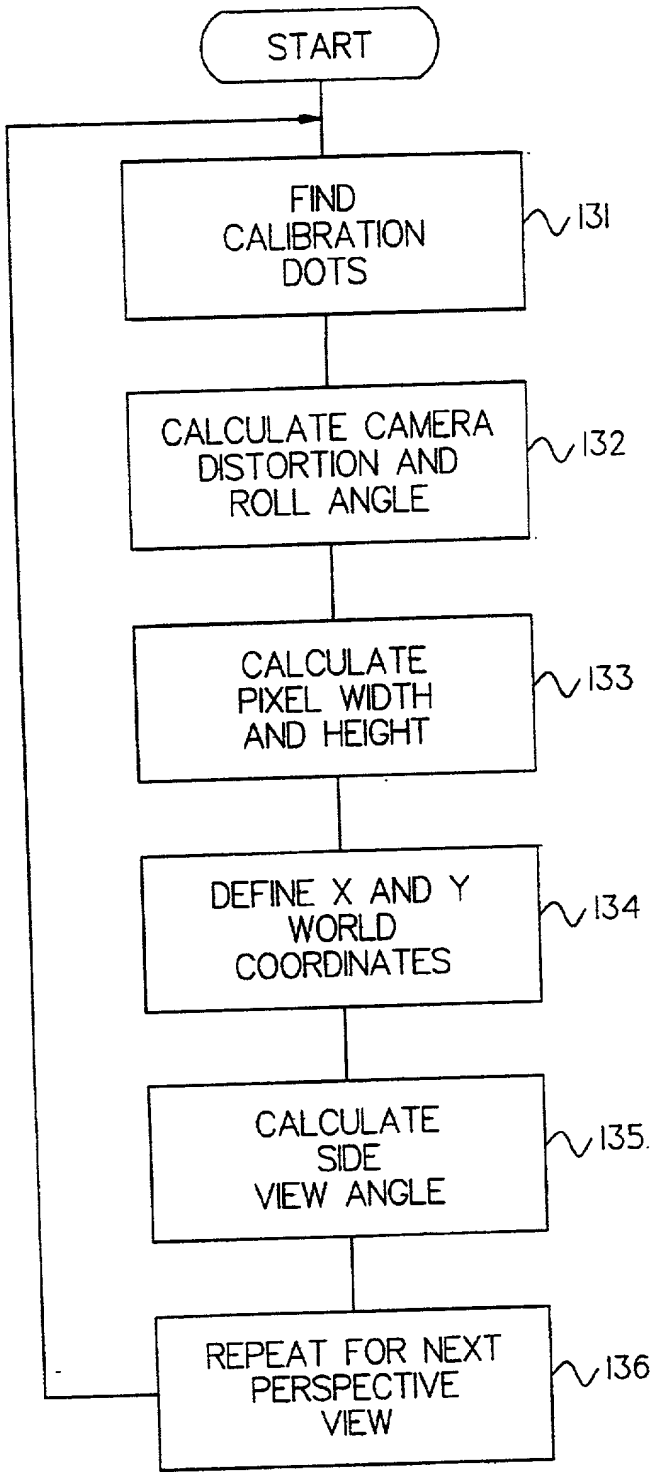

Fig_ 2D

SUBSTITUTE SHEET (RULE 26)

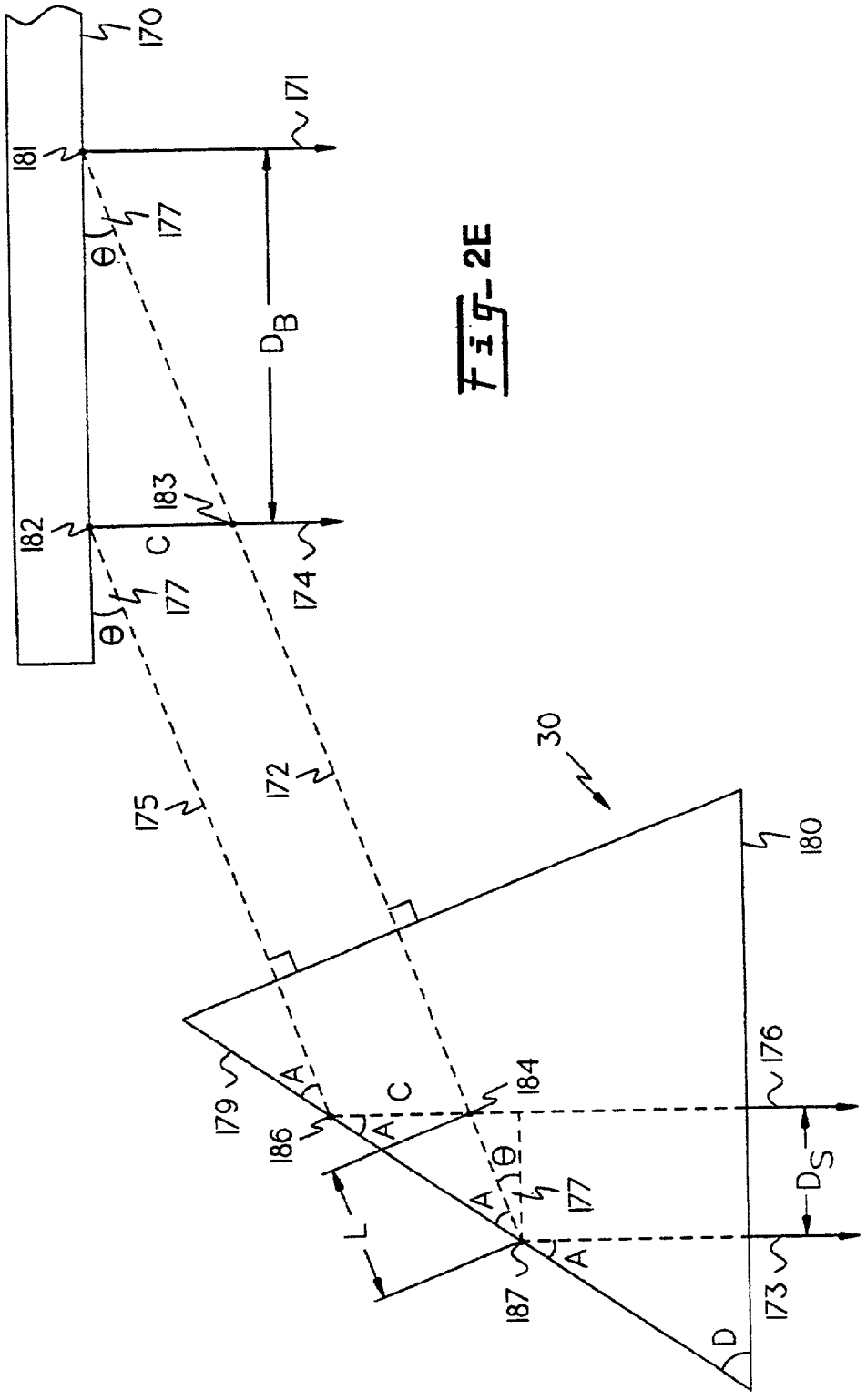

Fig-2E

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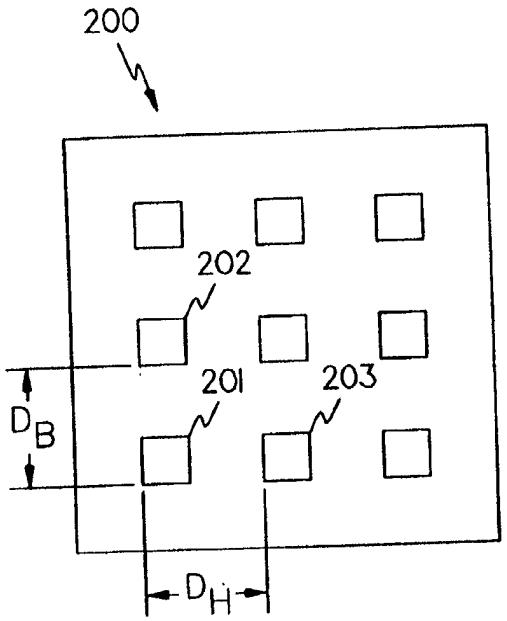
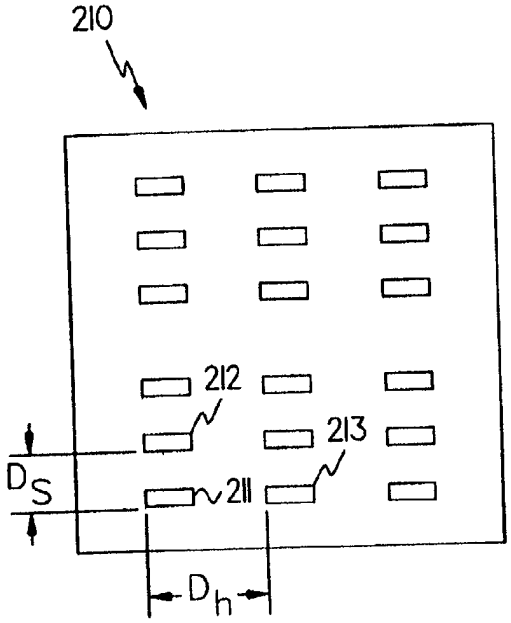


BOTTOM VIEW

Fig. 2F1



SIDE PERSPECTIVE VIEW

Fig. 2F2

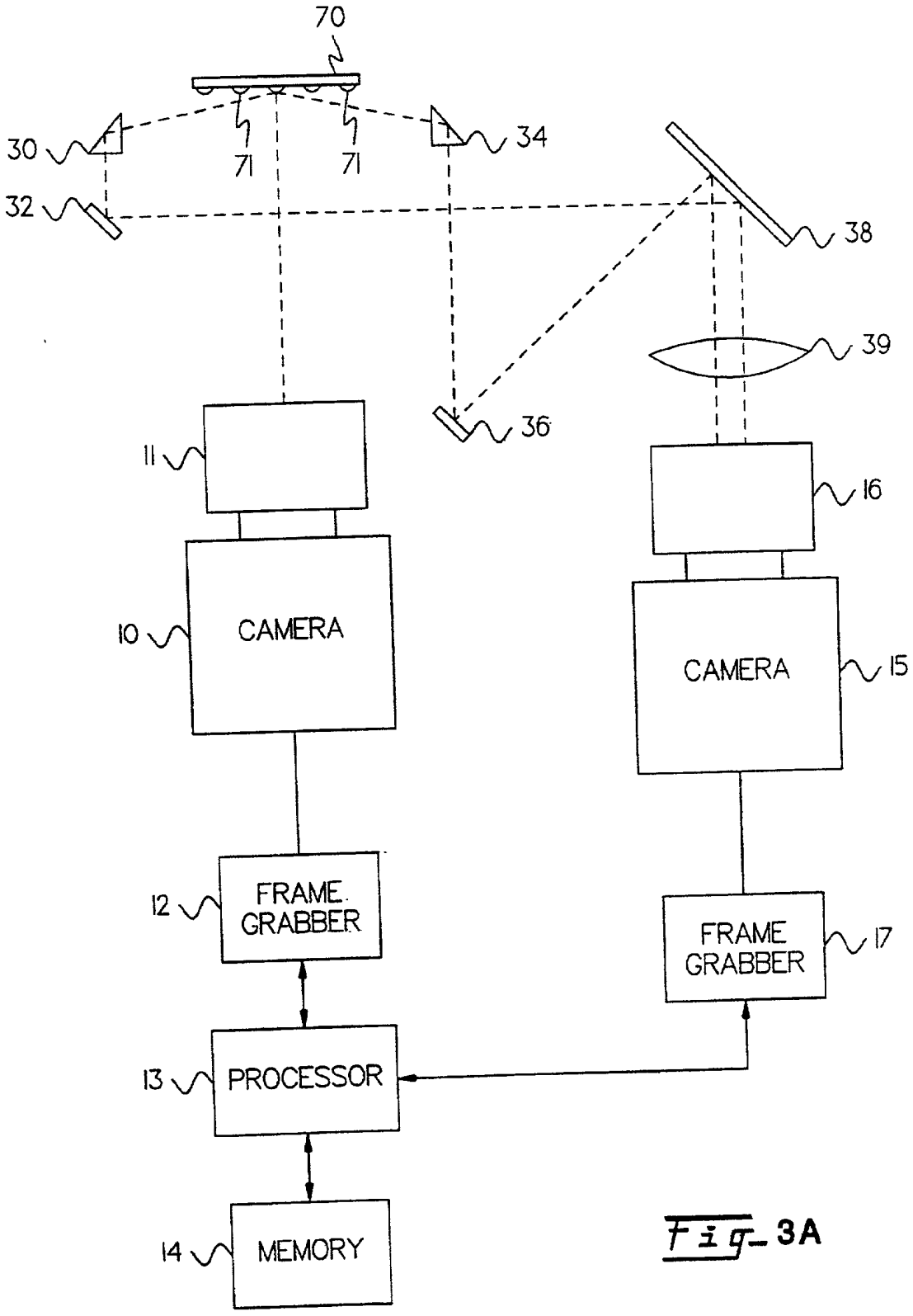


Fig. 3A

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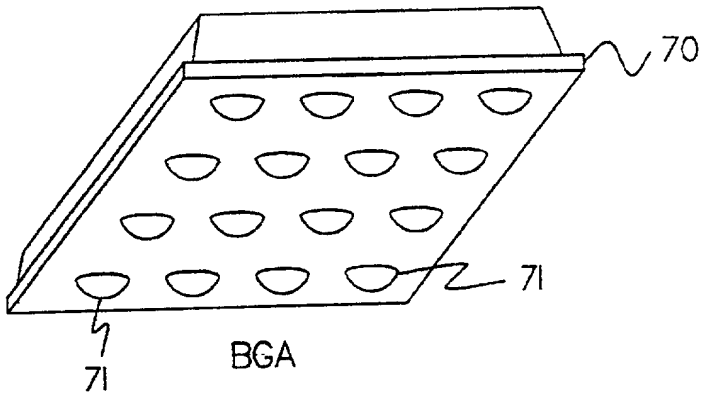

Fig_ 3B1

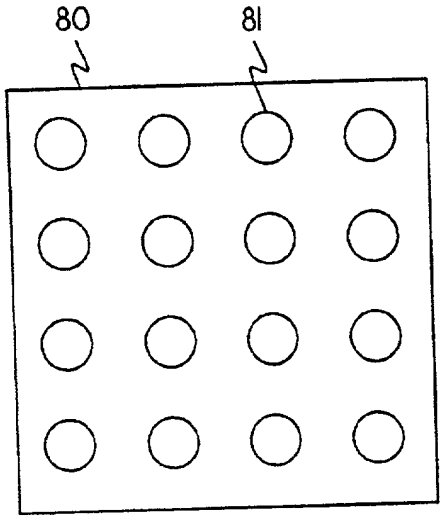

Fig_ 3B2

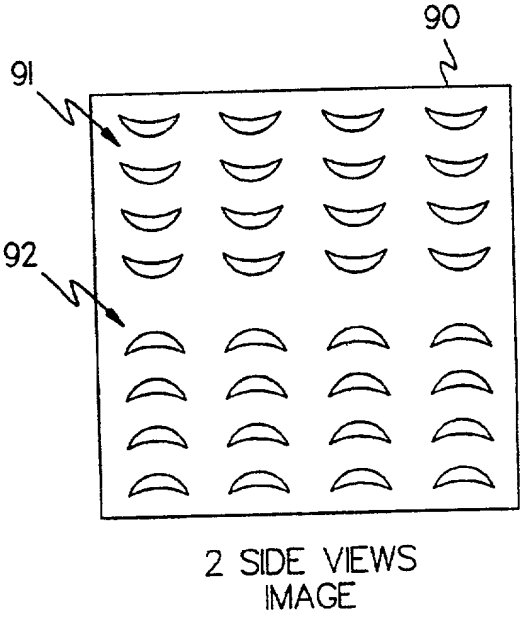

Fig_ 3B3

Docket No. 2000-0476

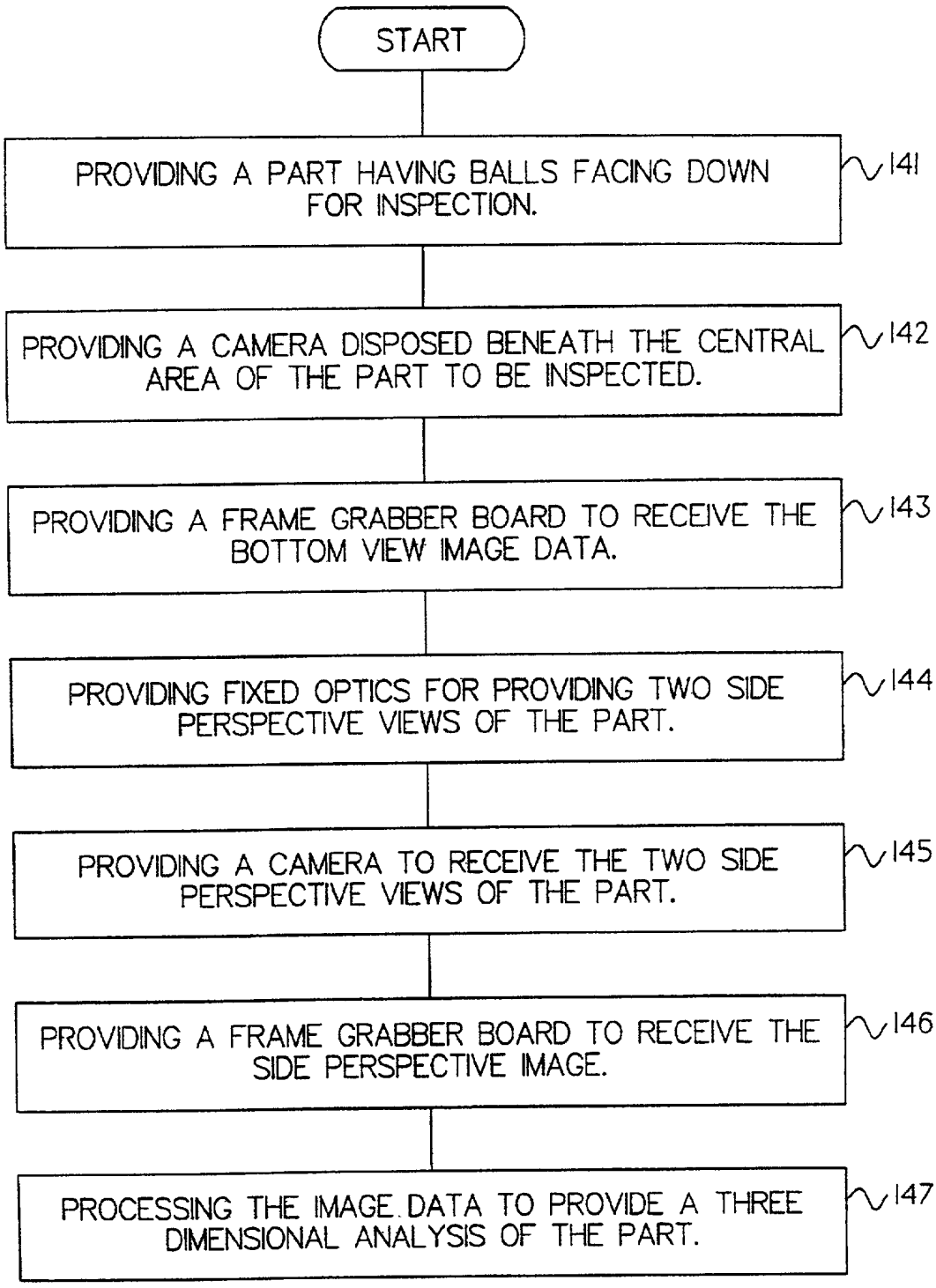

Fig. 4

EL 274 905 US

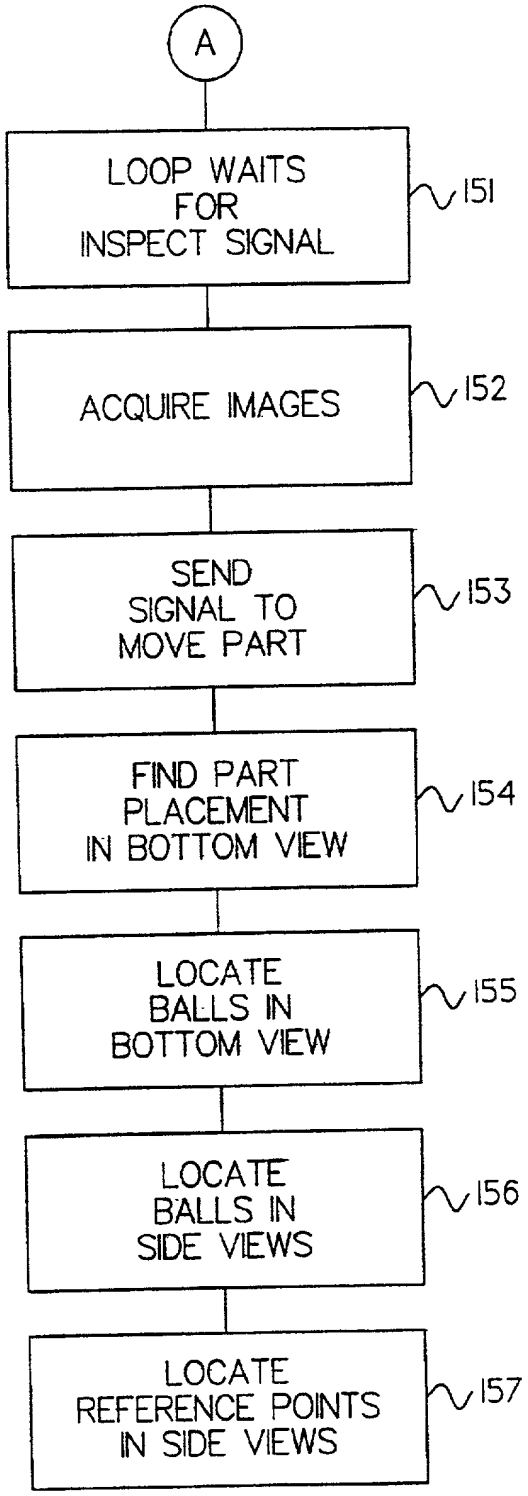

Fig. 5A

Docket No. 95-0459

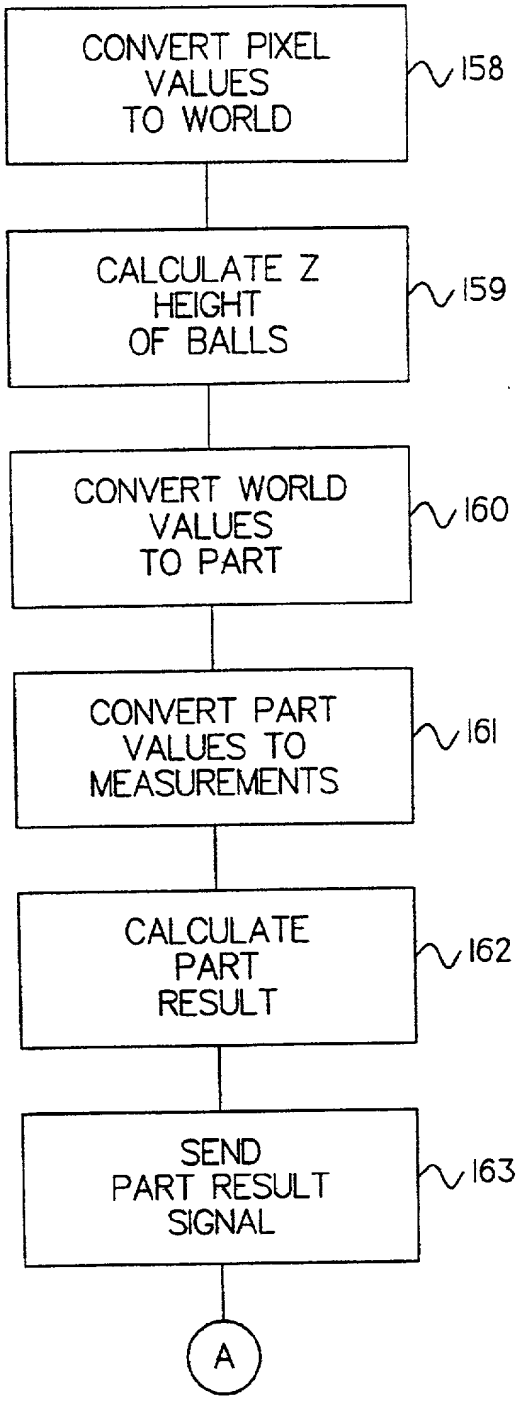

Fig. 5B

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TO/240" 929th860

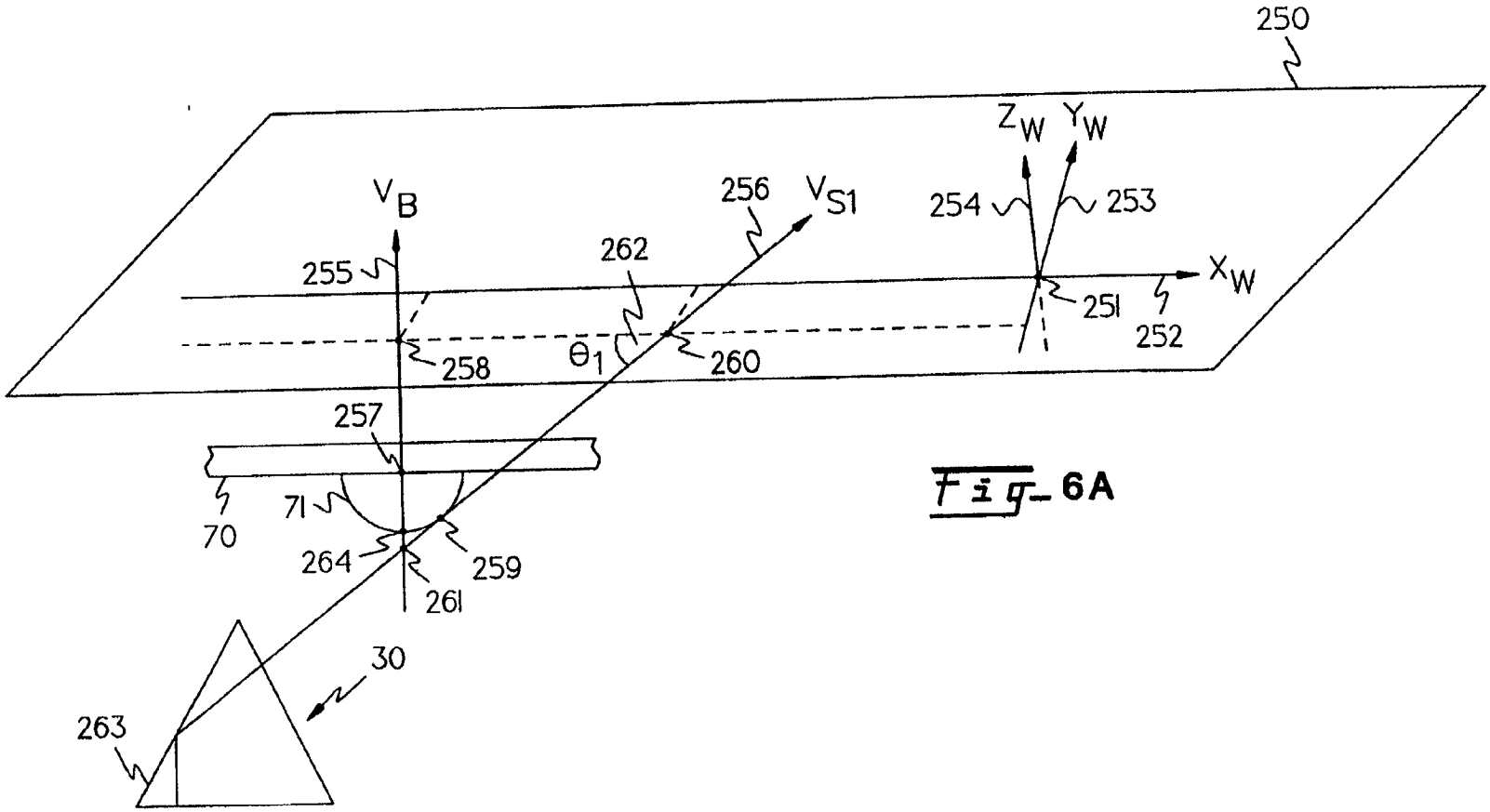

Fig. 6A

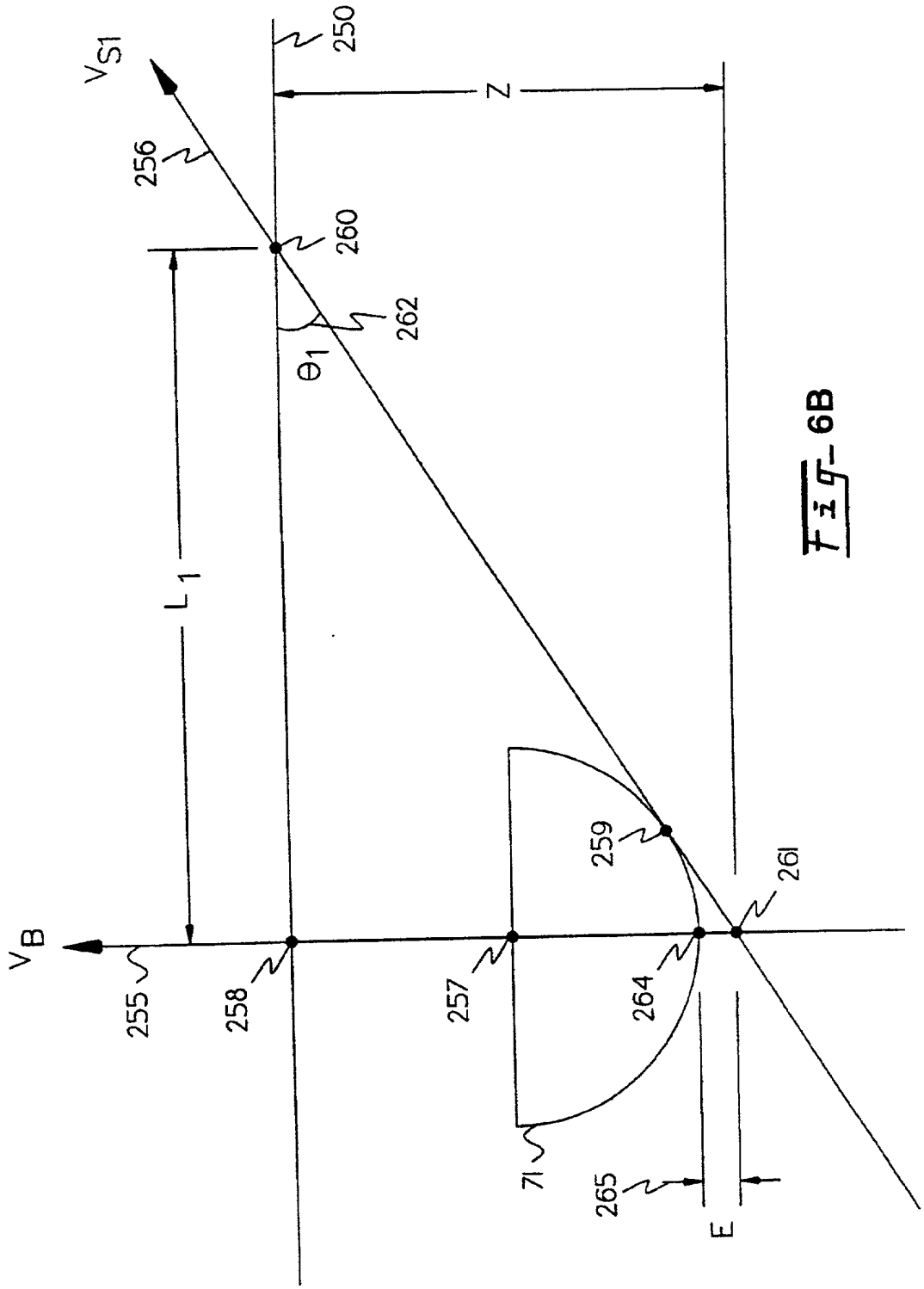
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Fig- 6B

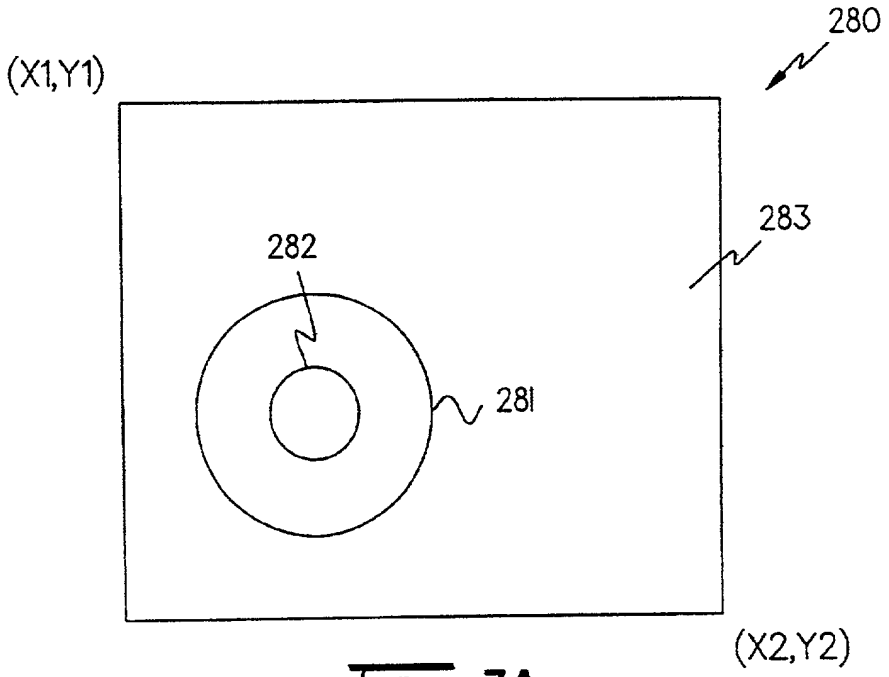

Fig_7A

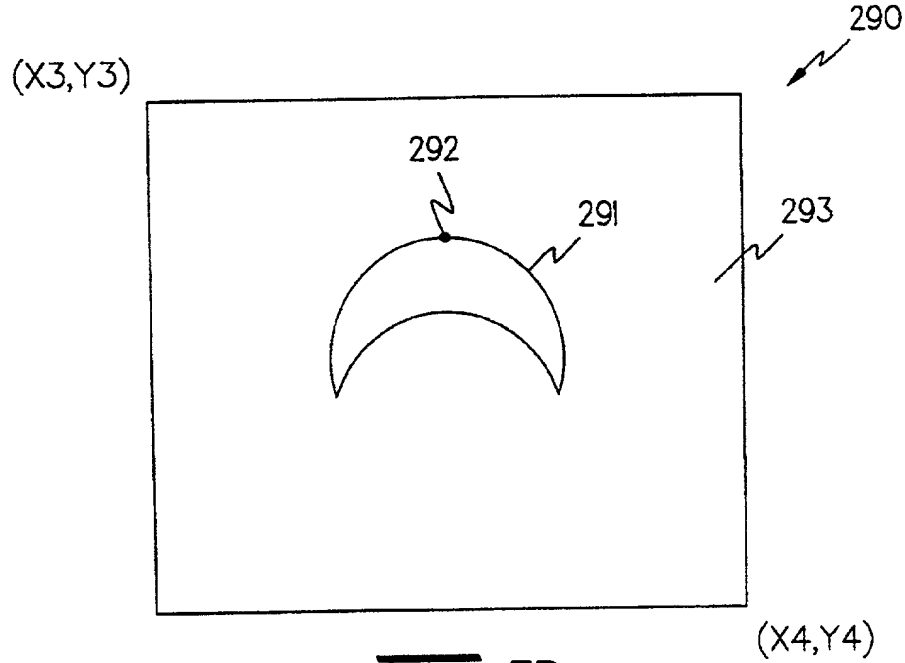

Fig_7B

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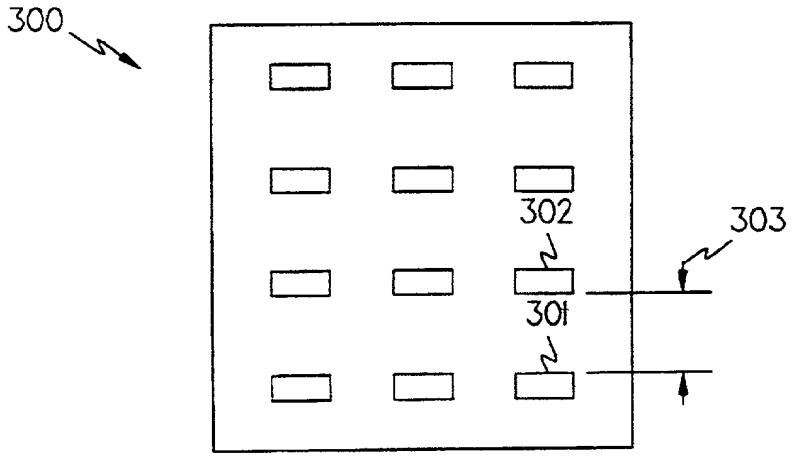
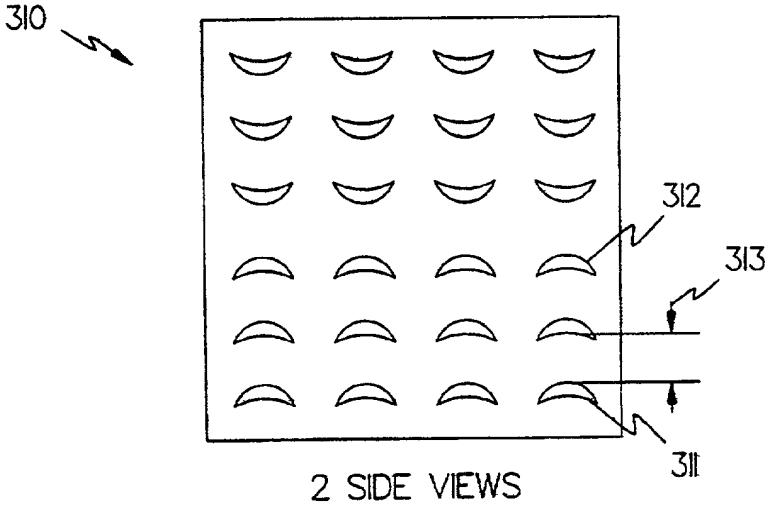


2 SIDE VIEWS
CALIBRATION PATTERN

Fig_ 8A



2 SIDE VIEWS
BALLS

Fig_ 8B

Patentansprüche

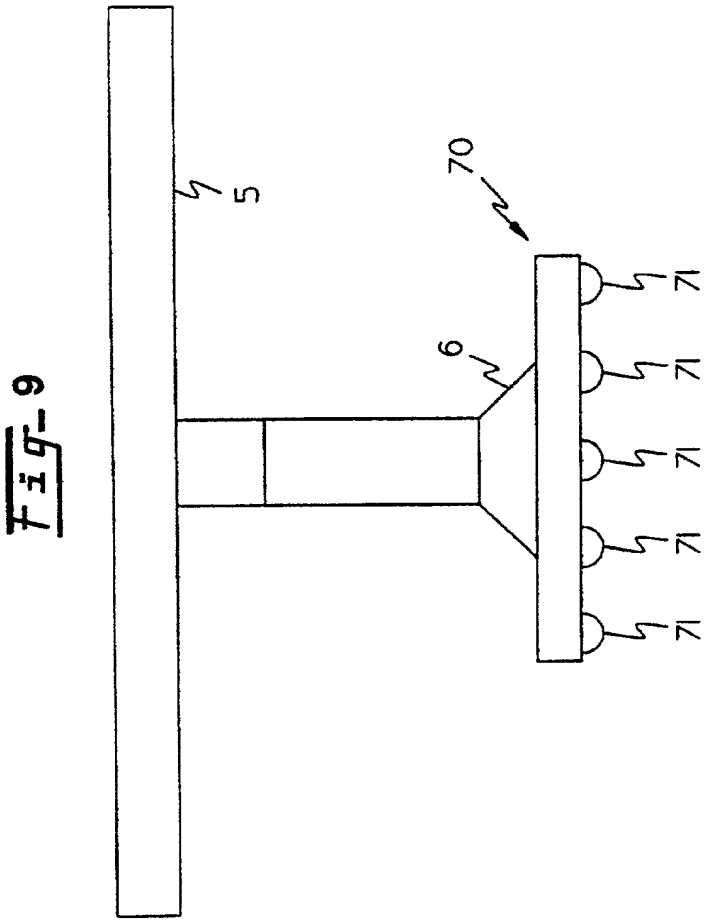


Fig. 9

TO/2h0" 929+h860

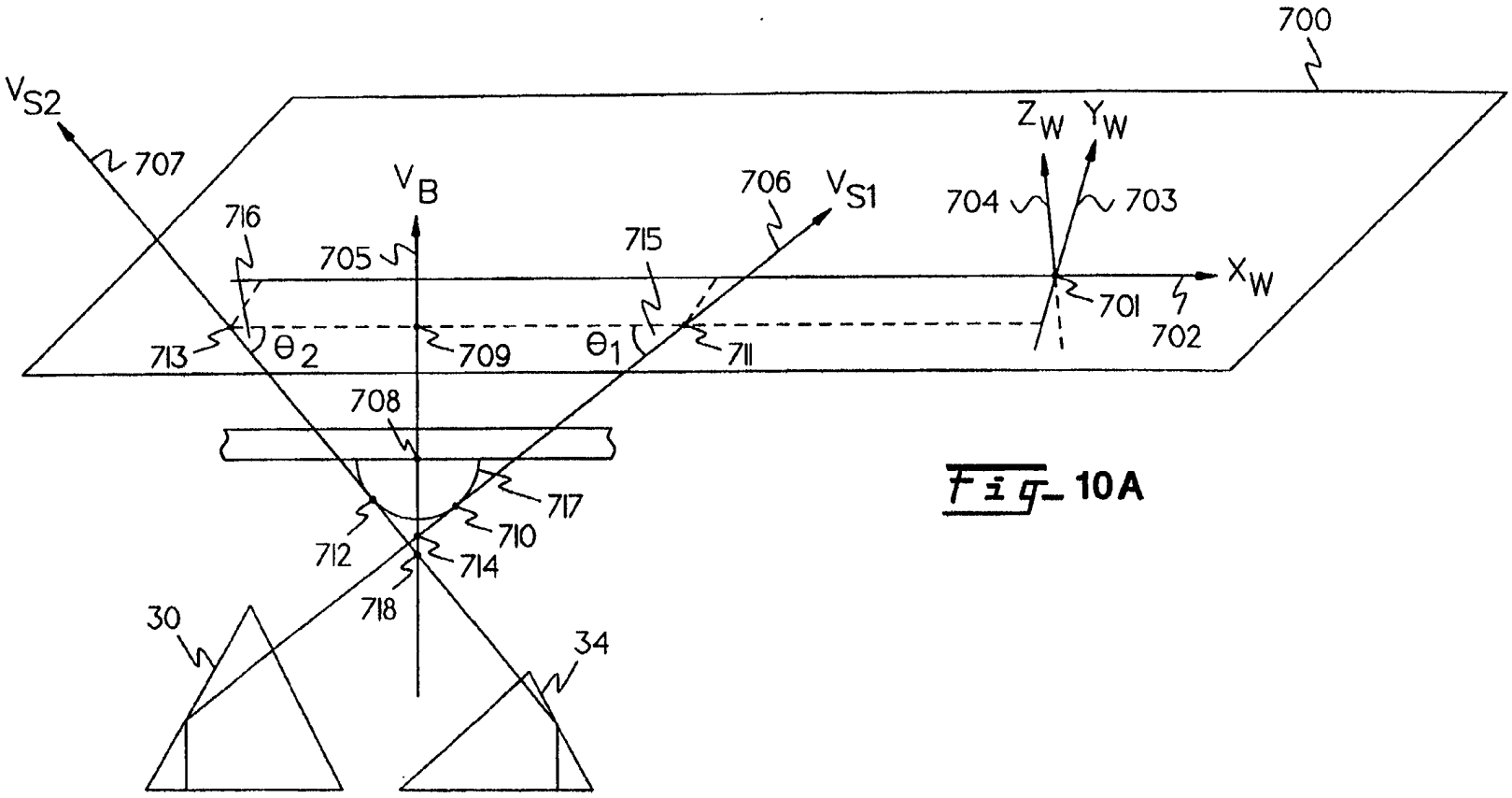


Fig_ 10A

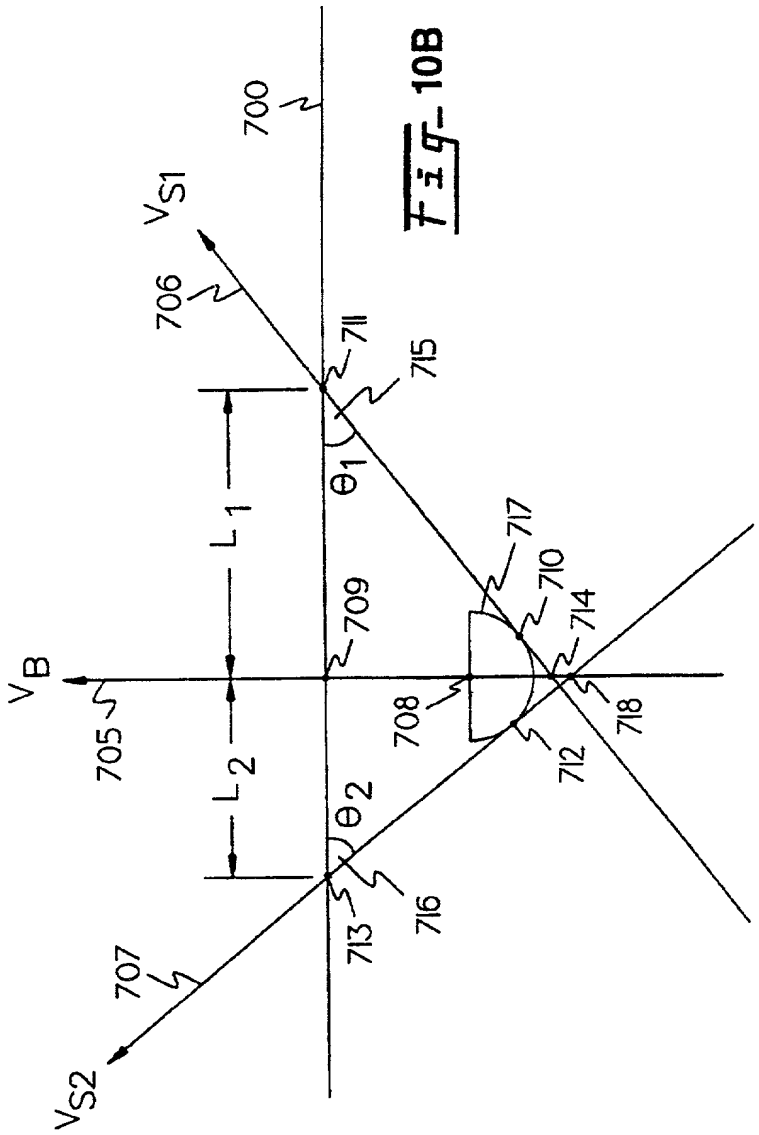

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Fig. 10B

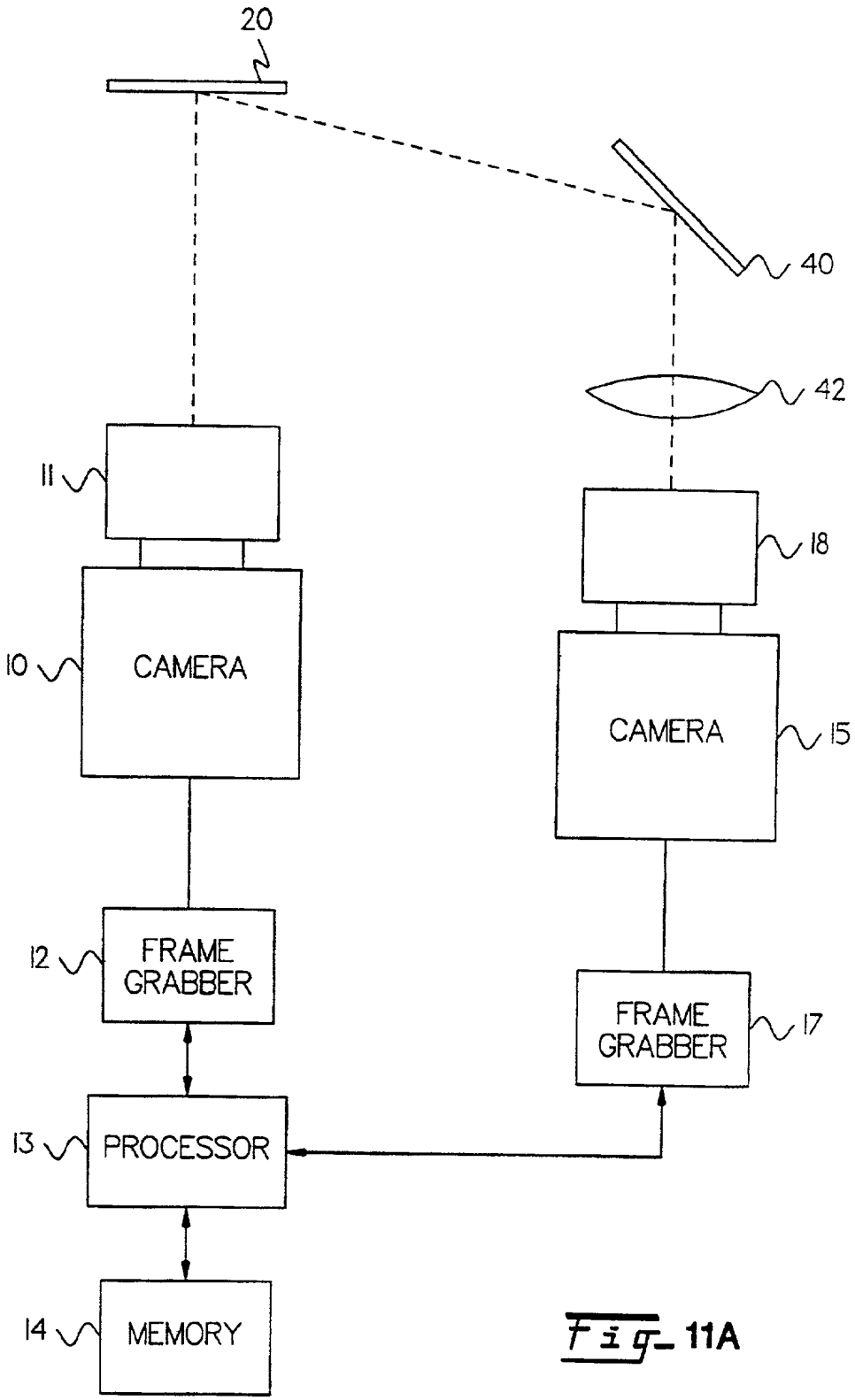
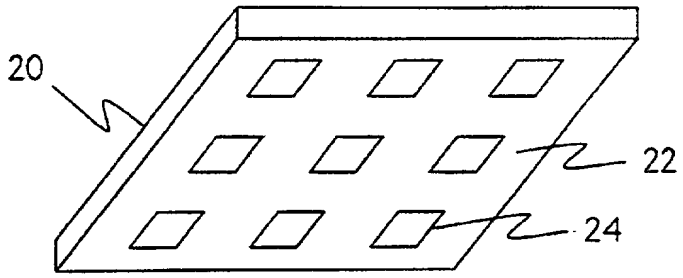


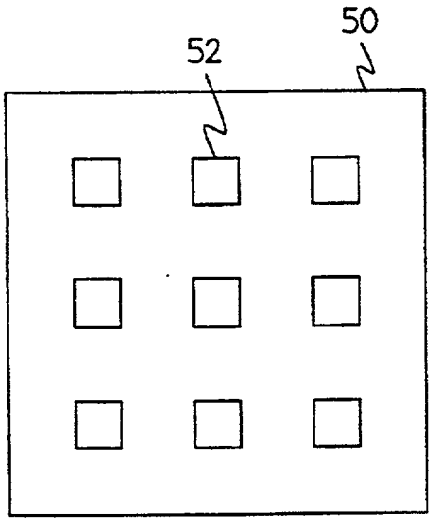
Fig. 11A

SUBSTITUTE SHEET (RULE 26)



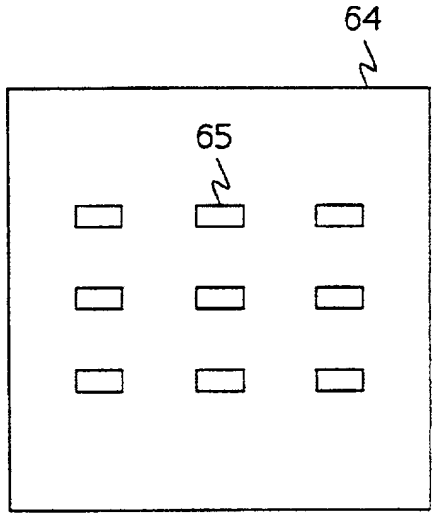
CALIBRATION
RETICLE

Fig. 11B1



BOTTOM VIEW IMAGE

Fig. 11B2



SIDE VIEW IMAGE

Fig. 11B3

EP 0 450 050 B1

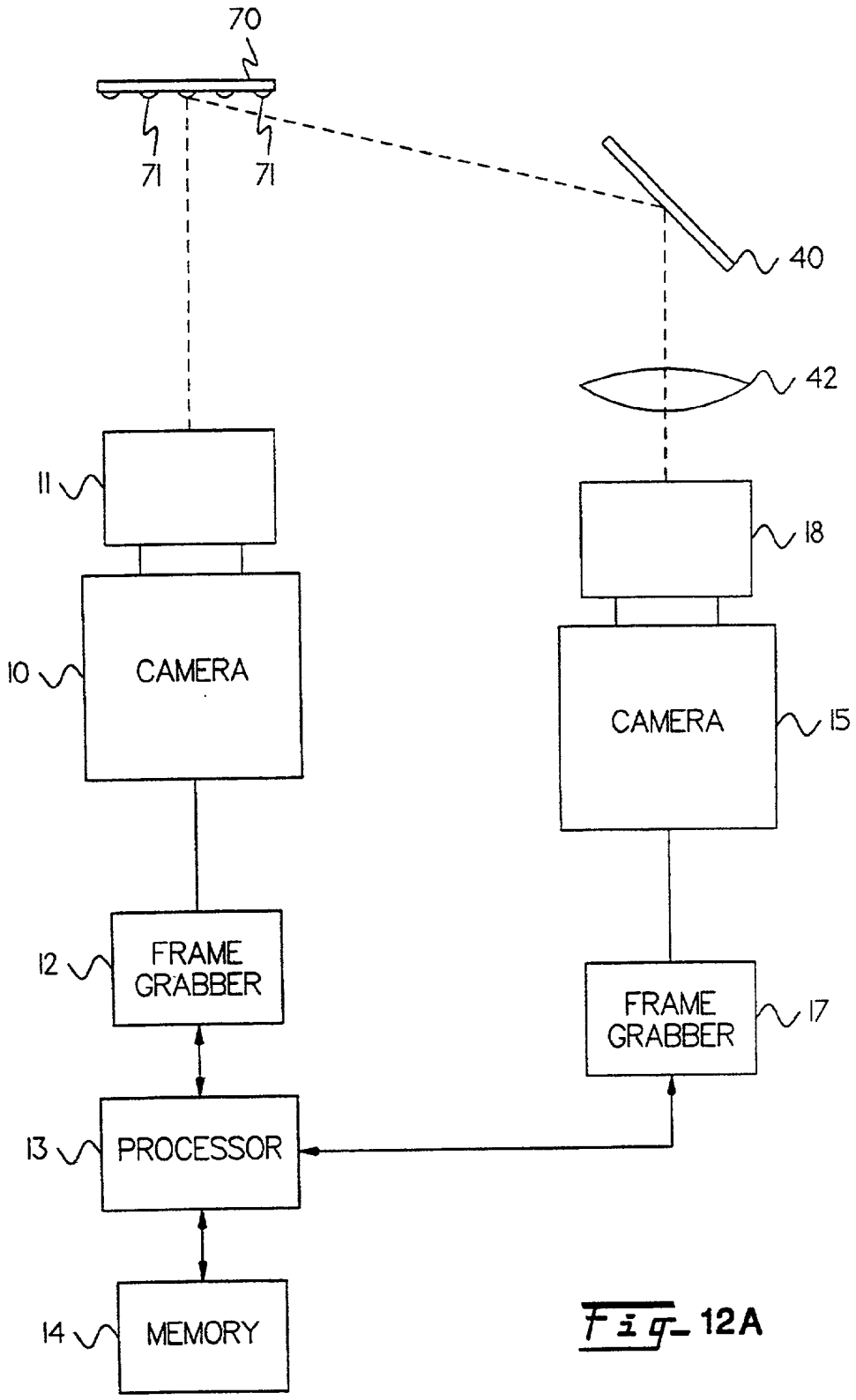


Fig. 12A

SUBSTITUTE SHEET (RULE 26)

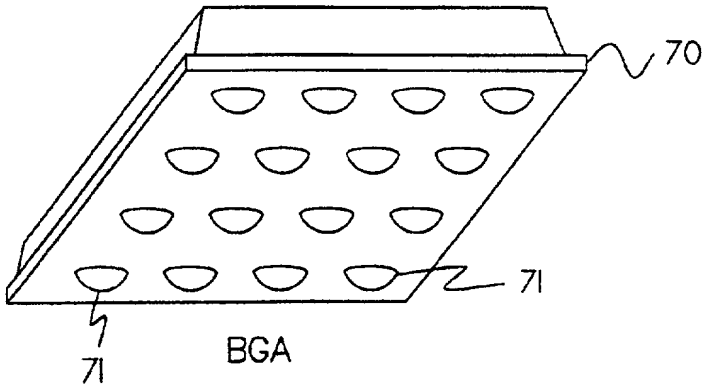
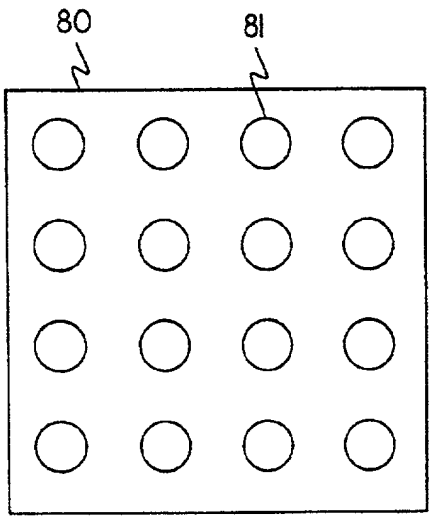
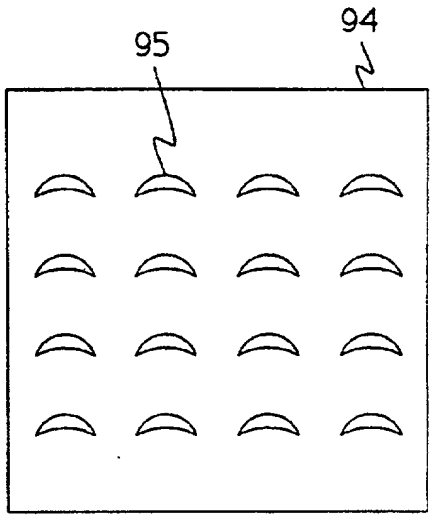


Fig_ 12B1



BOTTOM VIEW IMAGE

Fig_ 12B2



SIDE VIEW IMAGE

Fig_ 12B3

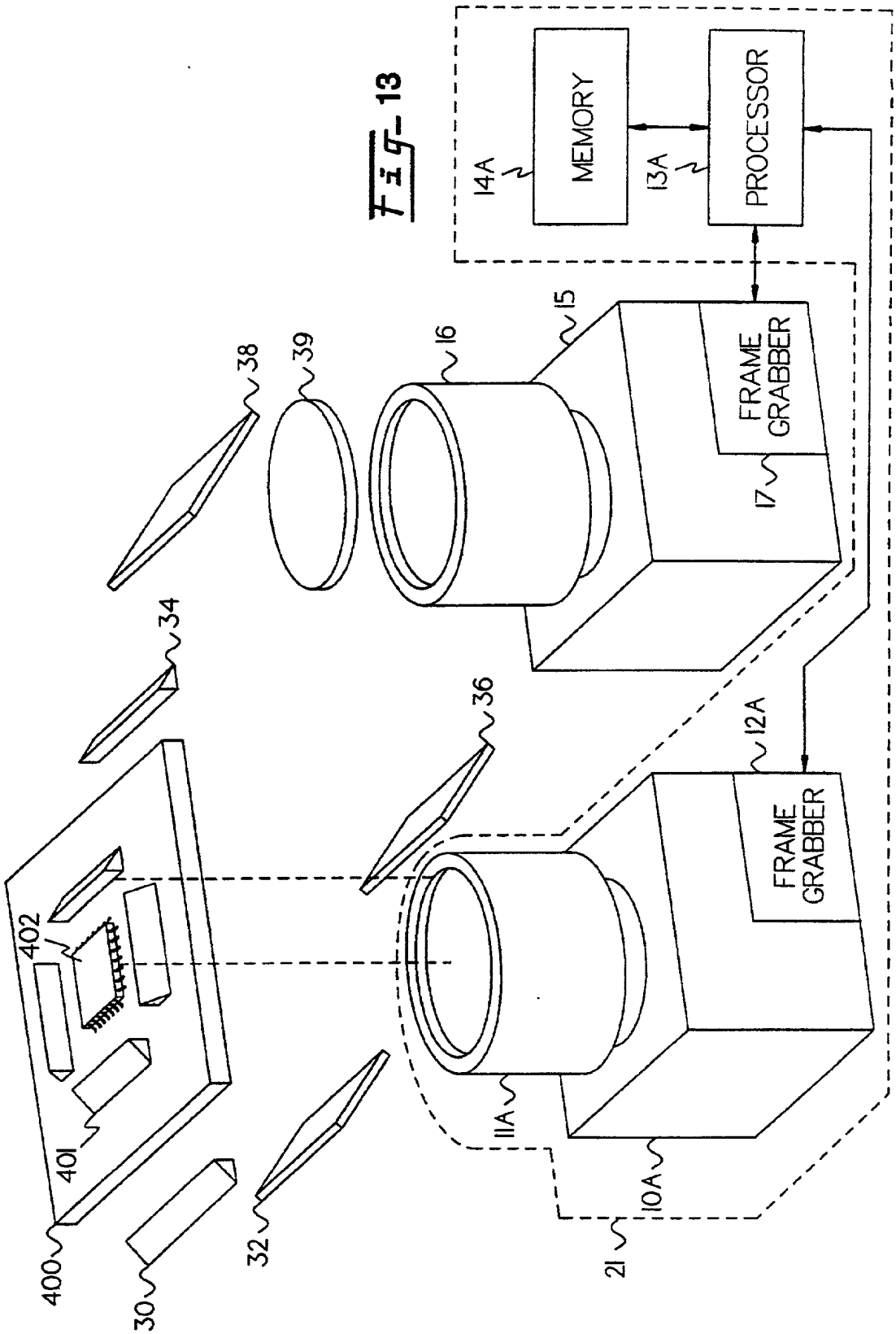

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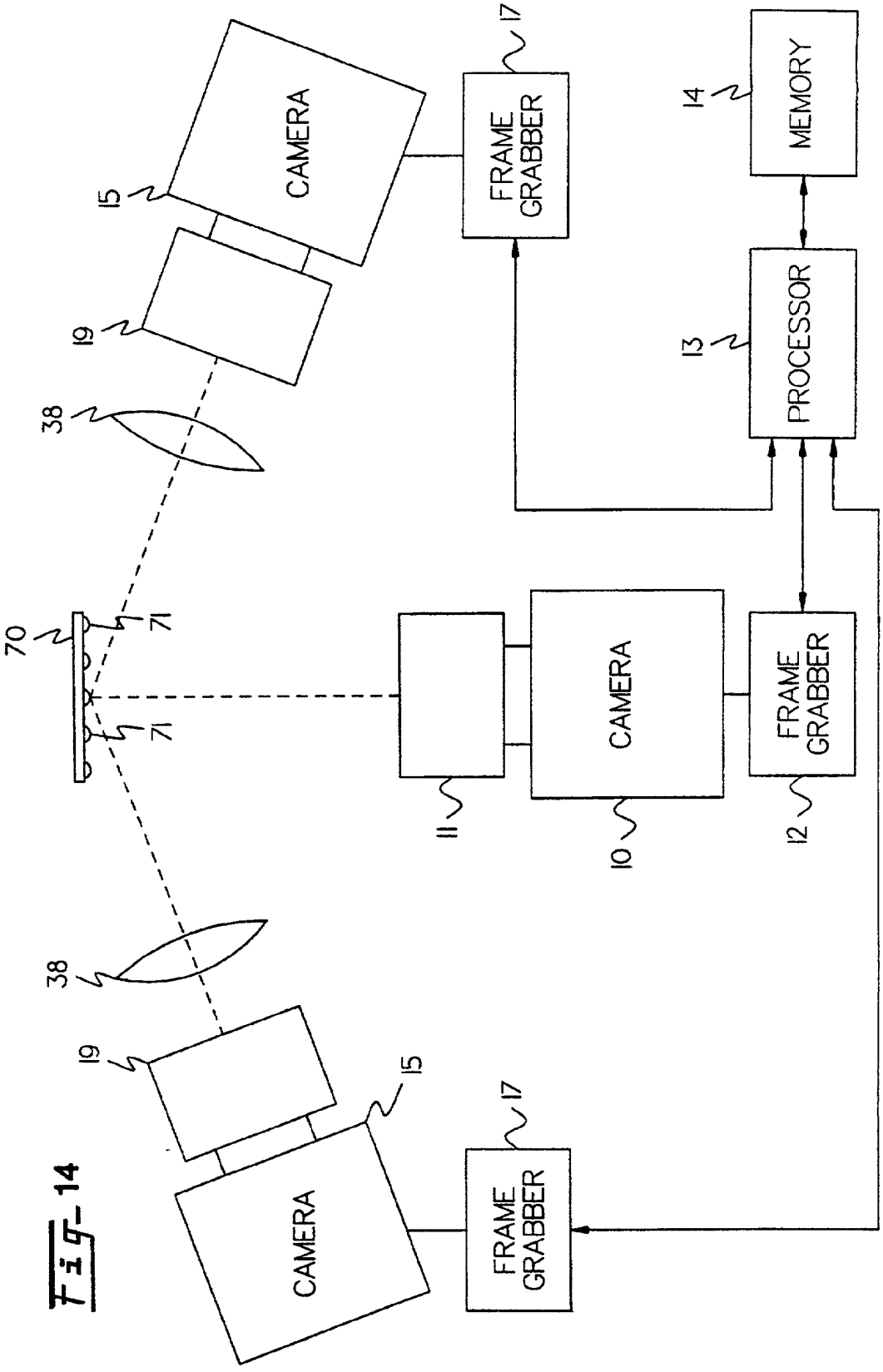
Fig- 13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MEMORY
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PROCESSOR

Fig- 14

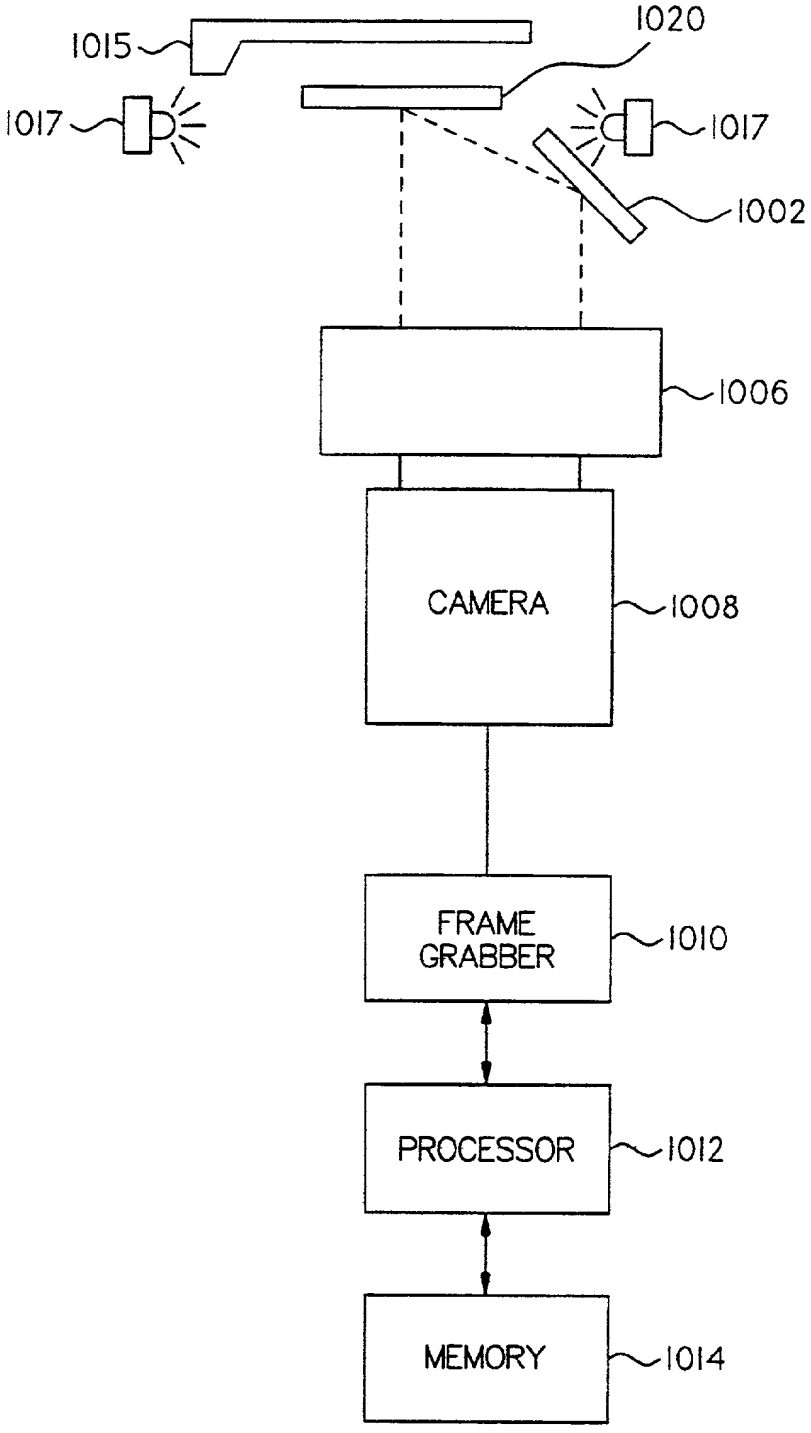

Fig. 15

WO 95/34044 PCT/US95/06574

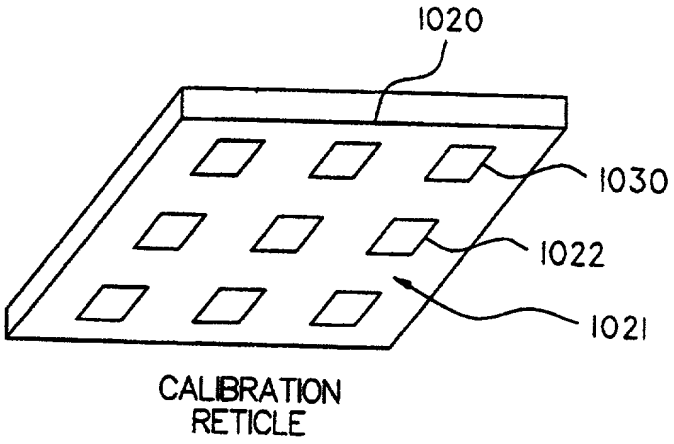

Fig. 16A

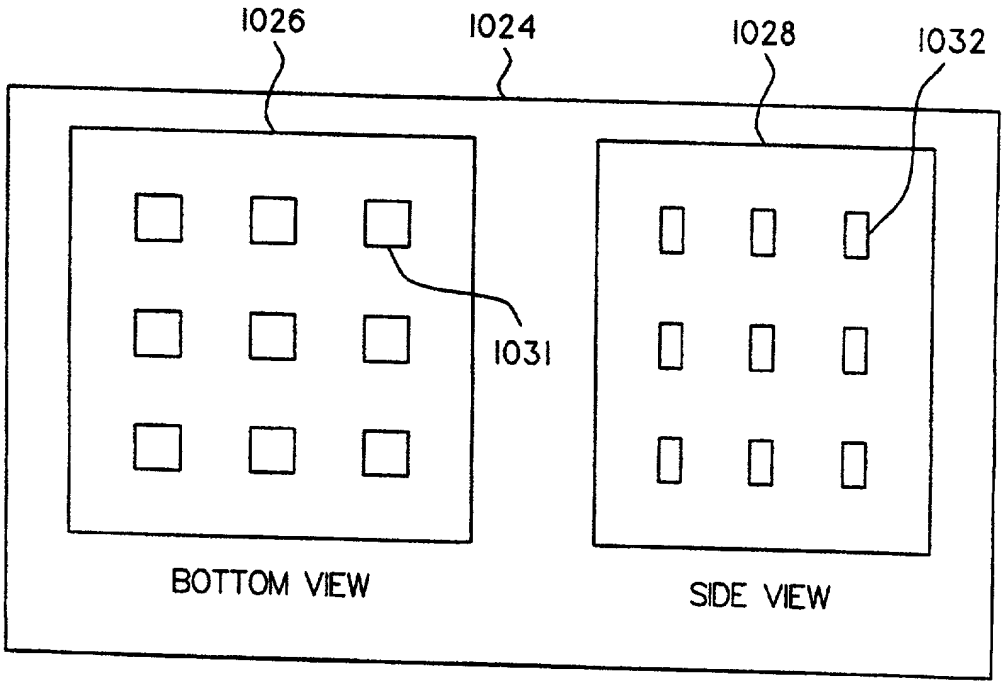

Fig. 16B

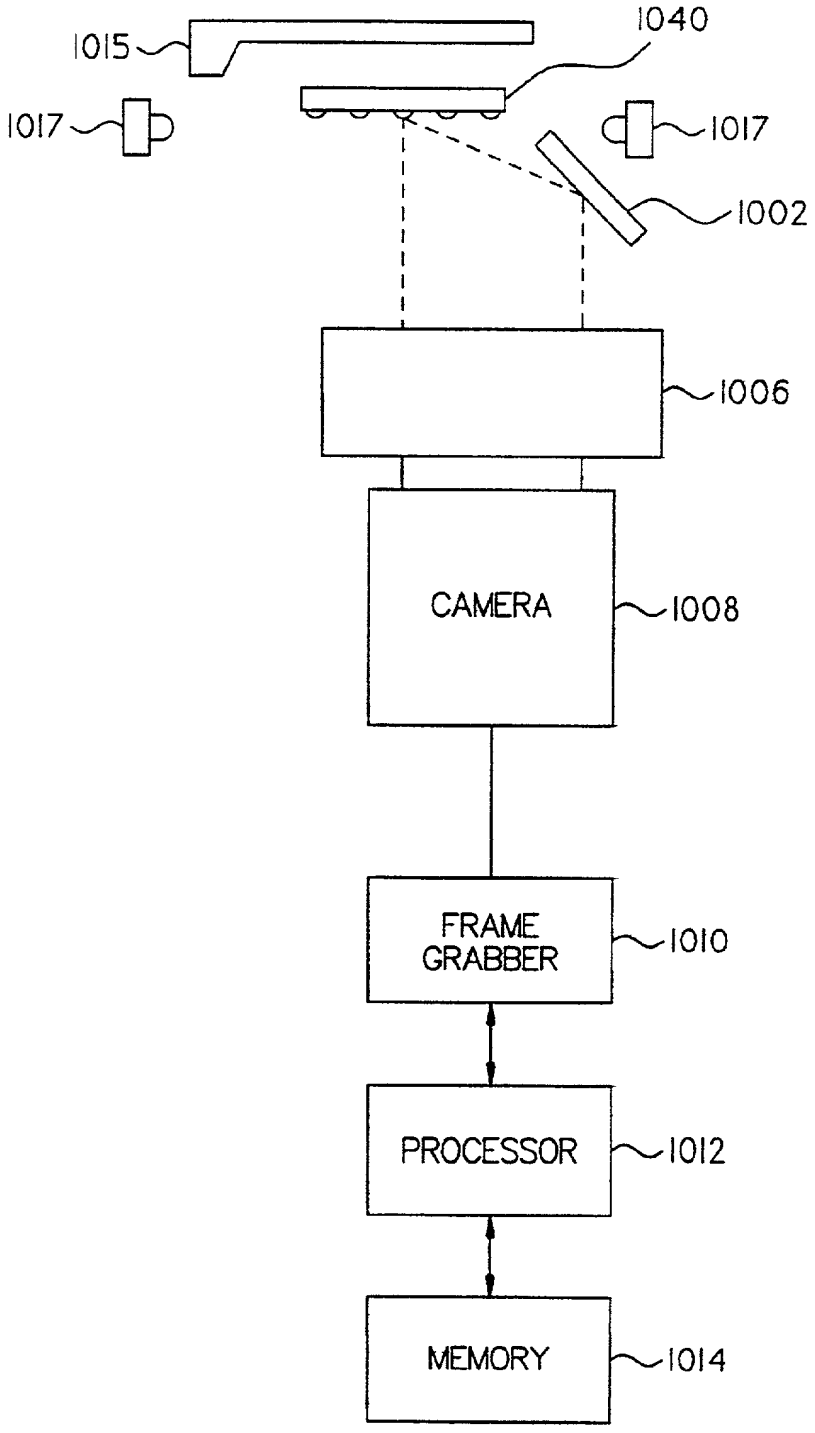

Fig. 17

SUBSTITUTE SHEET (RULE 26)

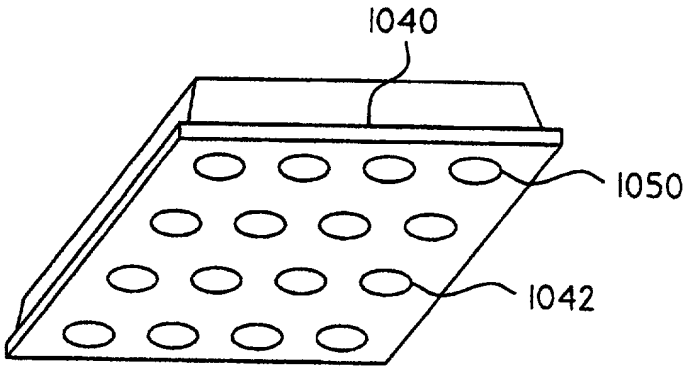

Fig. 18A

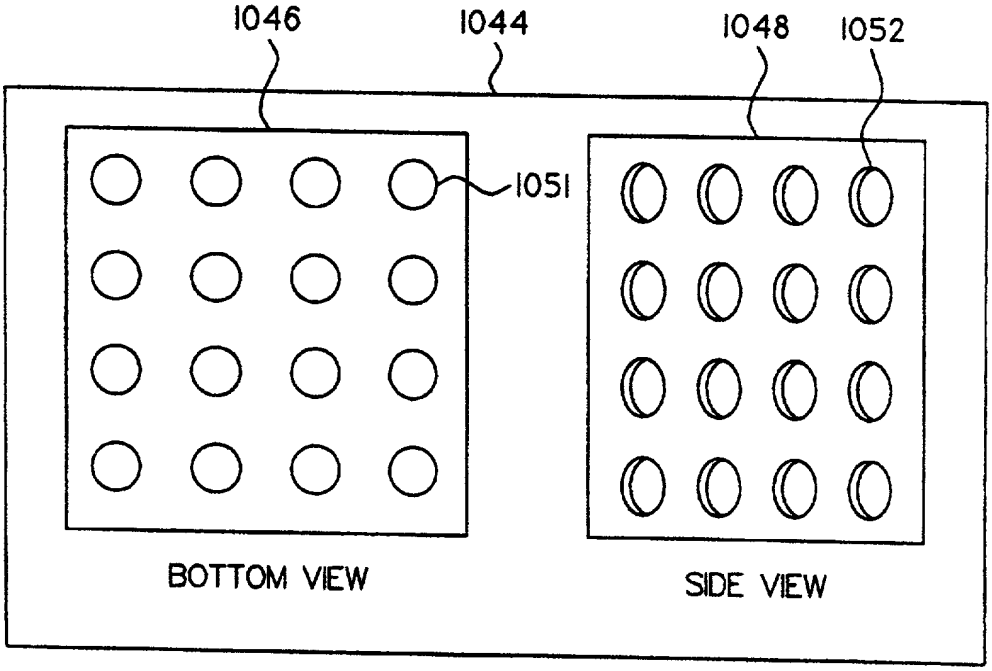

Fig. 18B

Docket: 5050-0426-0